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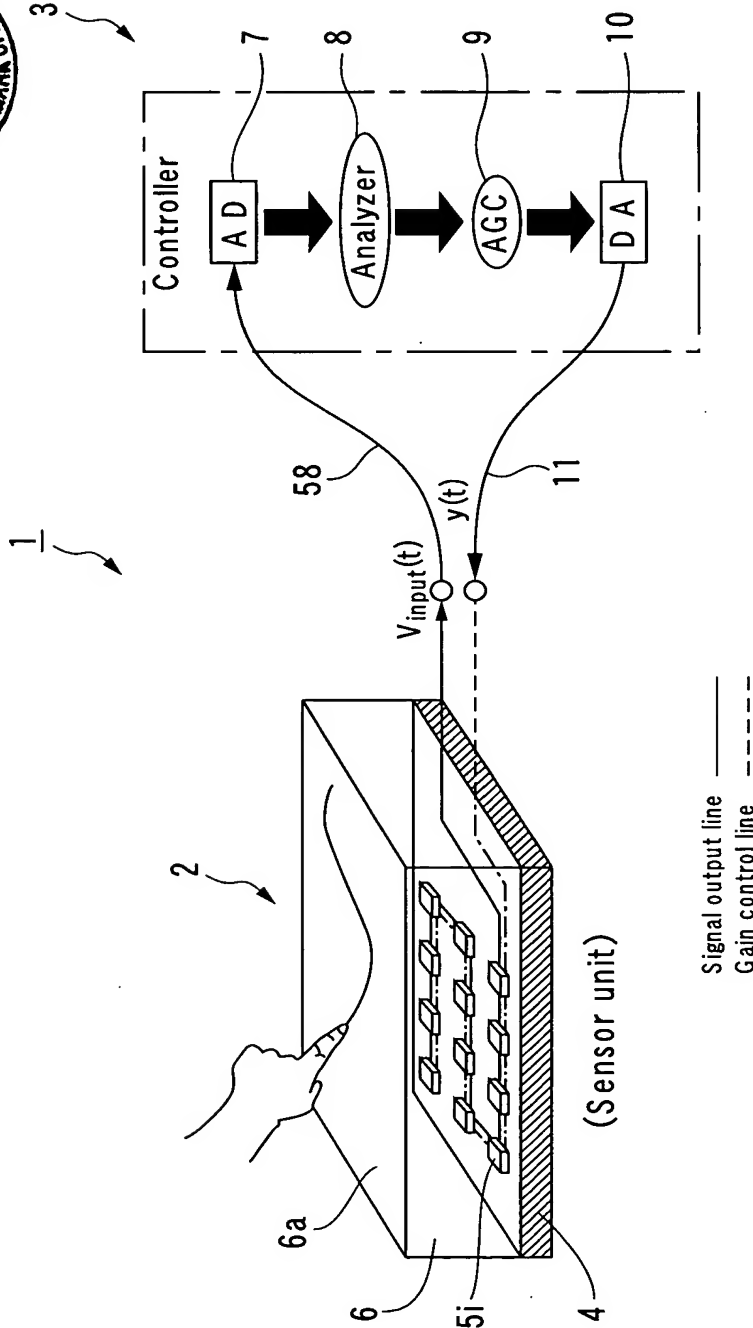


Fig.2

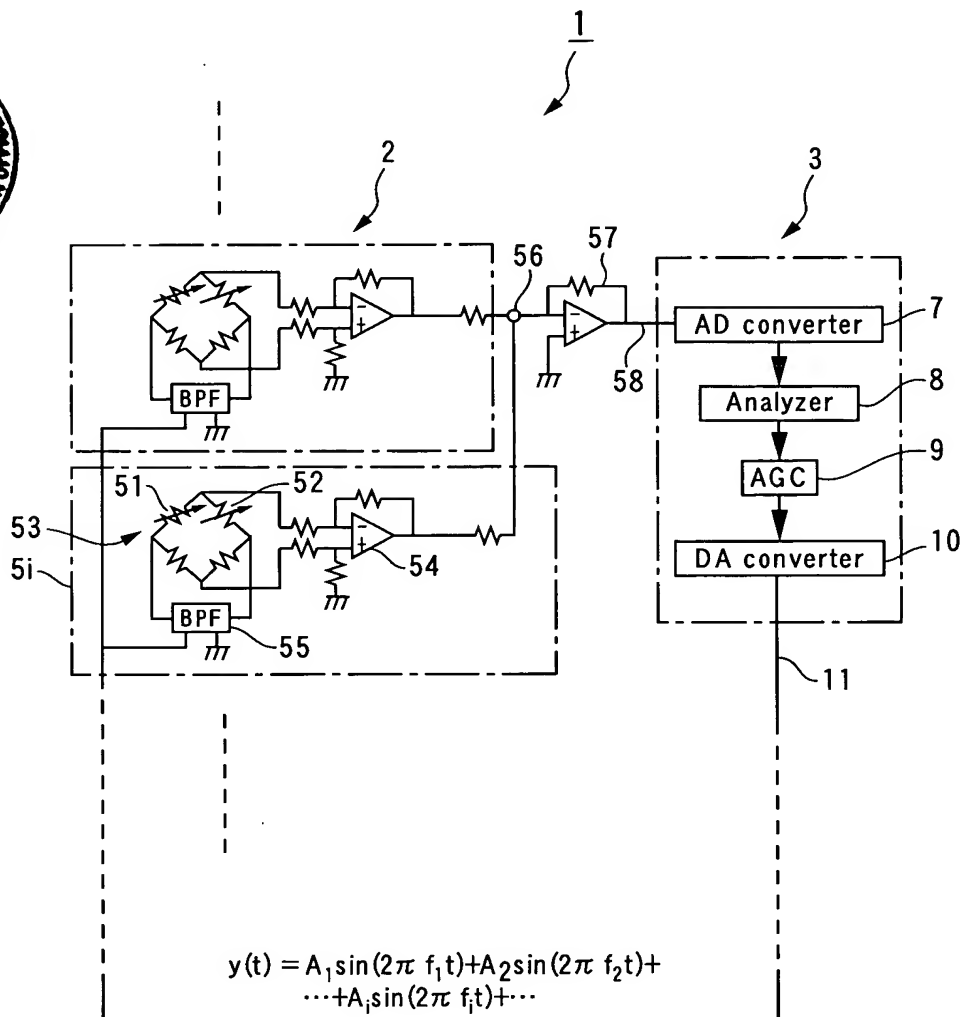
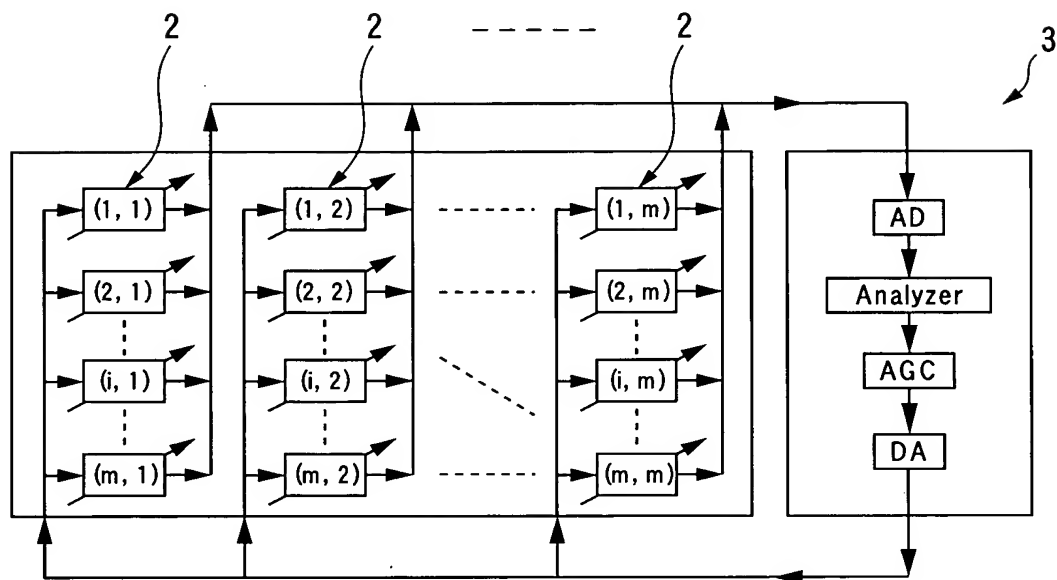




Fig.3



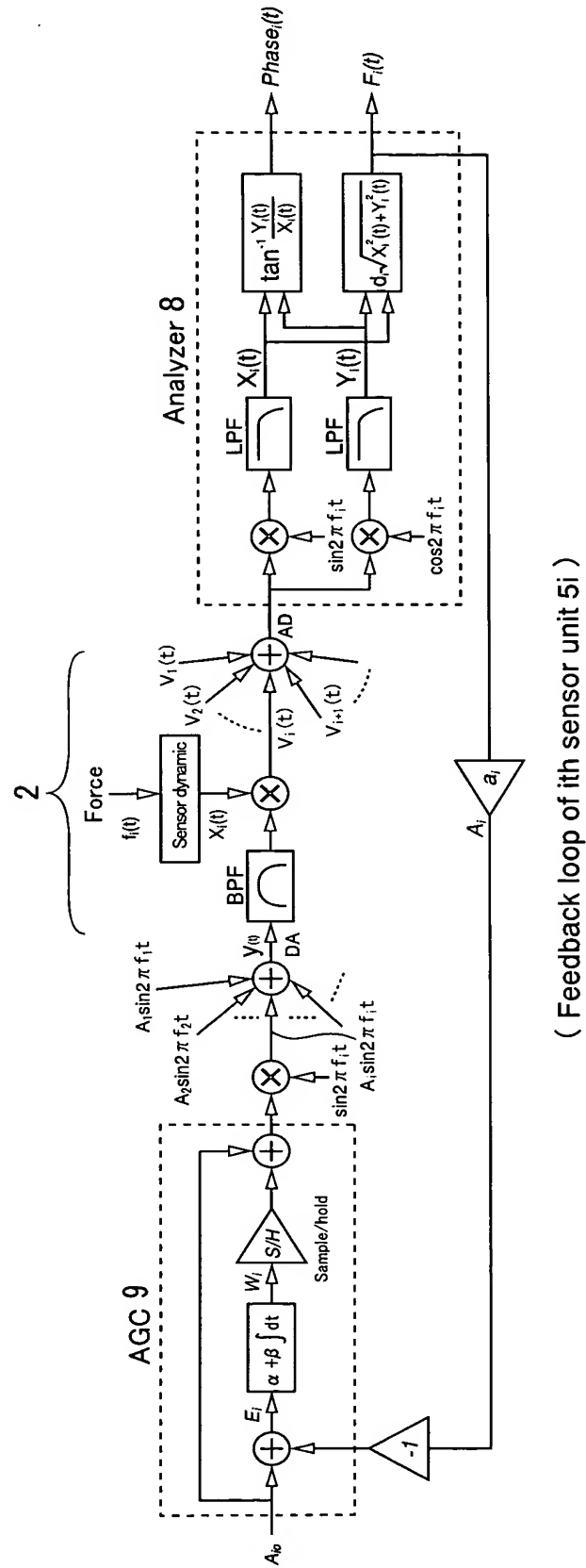


Fig.4



Fig. 5A

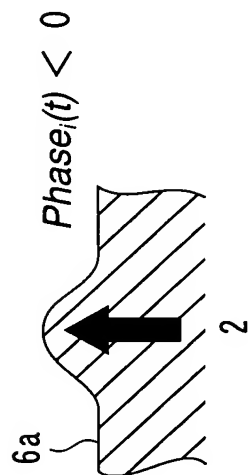
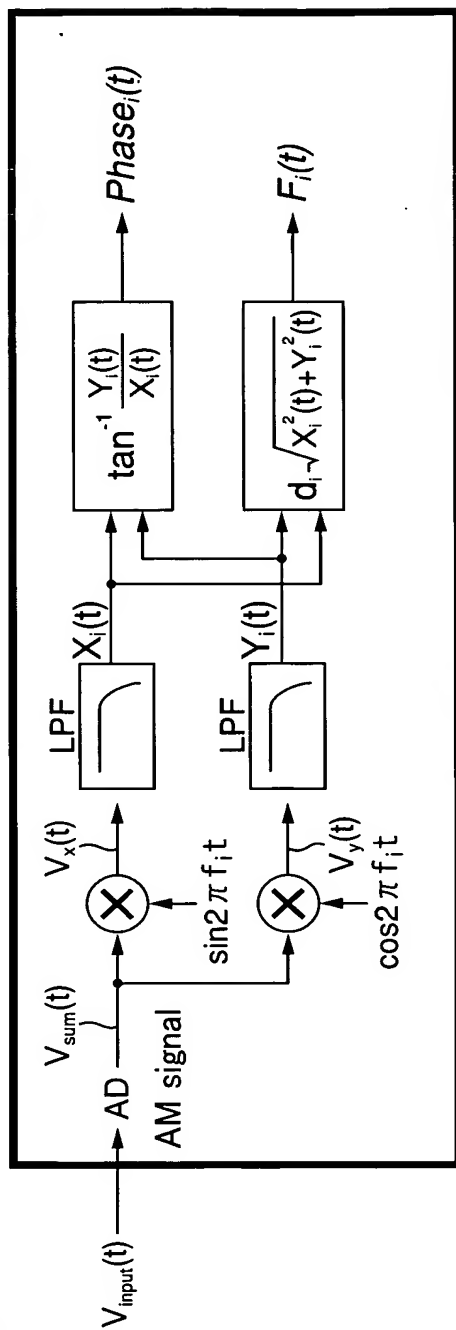


Fig. 5C

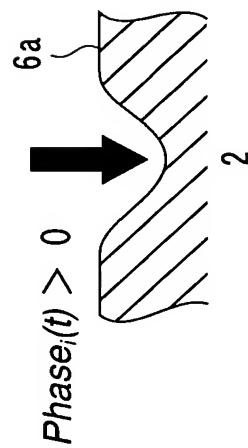


Fig. 5B



Fig. 6

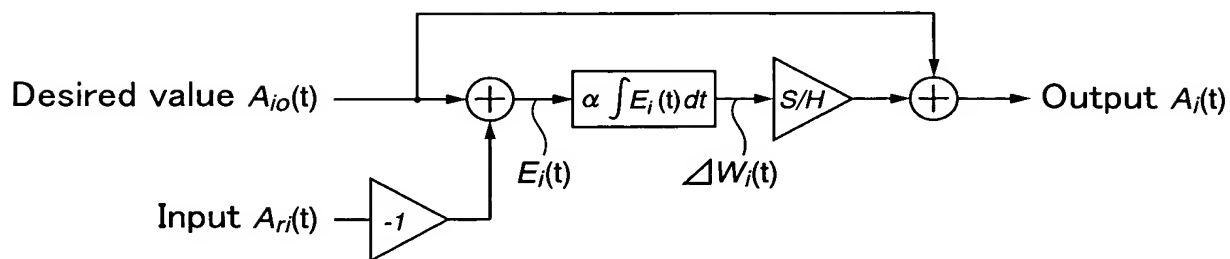
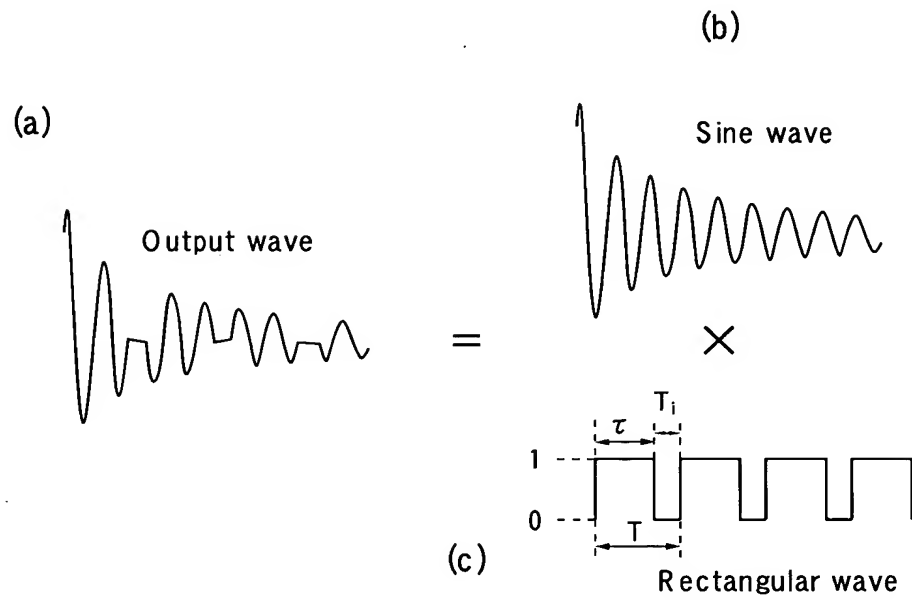




Fig. 7



(Controller output signal)





Fig. 8

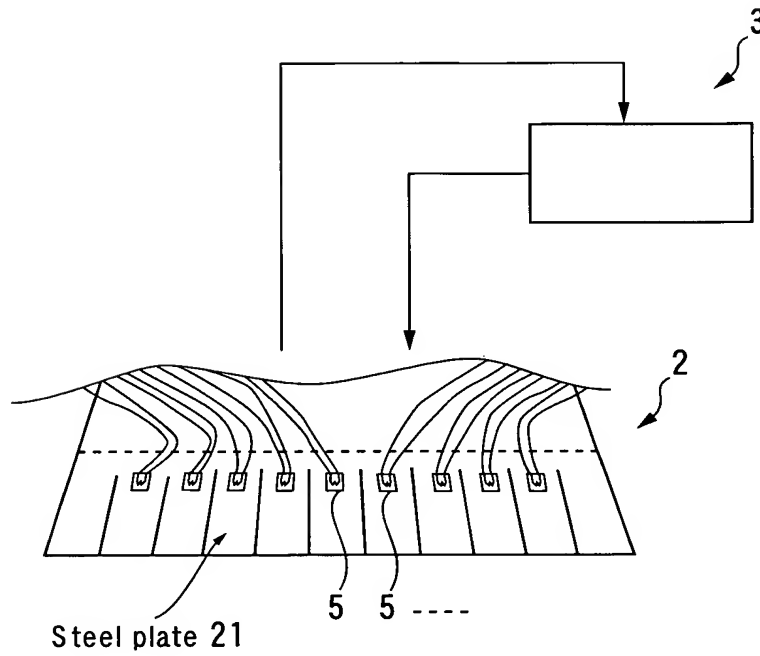




Fig. 9A

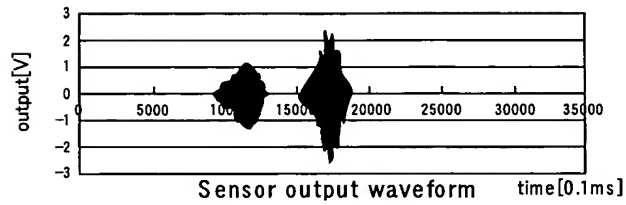


Fig. 9B

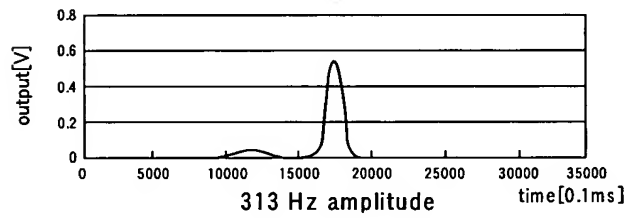


Fig. 9C

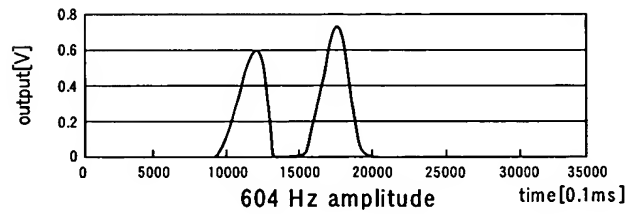




Fig. 10A

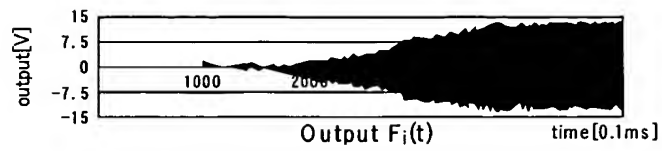


Fig. 10B

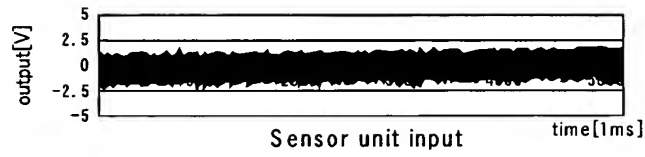




Fig. 11A

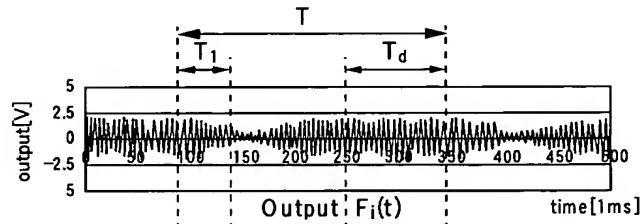


Fig. 11B

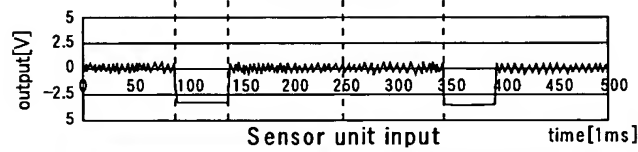


Fig. 11C

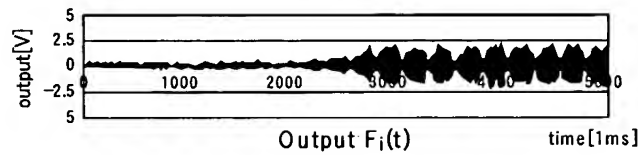


Fig. 11D

